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DRIVING CIRCUIT OF A LIQUID CRYSTAL DISPLAY AND DRIVING METHOD THEREOF

Abstract

The present invention relates to a driving circuit of a liquid crystal display and a driving method thereof. The method includes receiving a M-bit image data from an image data input terminal and extracting N most significant bits (MSB)of the M-bit image data to form a N-bit image data. The N-bit image data is delayed by one frame period to form a N-bit delayed image data. The N-bit delayed image data is compared with P MSB of a current M-bit image data to determine whether to generate a first data voltage according to a first image value selected from a reference table, or to generate a second data voltage according to the current M-bit image data.